



2123

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : William R. Wheeler et al.  
Serial No.: 10/054,179  
Filed : January 17, 2002  
Assignee : Intel Corporation  
Title : MODELING A LOGIC DESIGN

Art Unit: 2123  
Examiner:

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Attorney's Docket No.:10559-607001

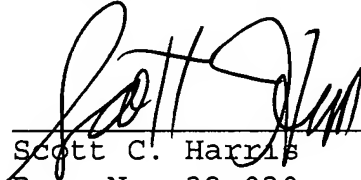
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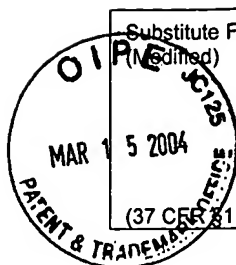
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Patent and Trademark OfficeAttorney's Docket No.  
10559-607001Application No.  
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(37 CFR 1.98(b))

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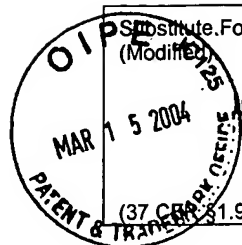
**U.S. Patent Documents**

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	AB	4,703,435	10/1987	Darringer, et al.			
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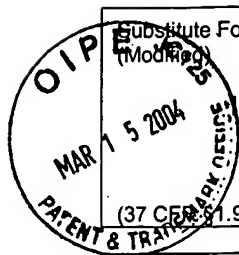
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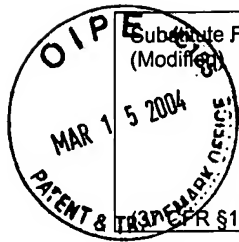
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Examiner Initial	Desig. ID	Document Number	Publication Date	Country or Patent Office	Class	Subclass	Translation	
							Yes	No
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	ATTT	0 720 233	07/1996	EPO				
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	AZZZ	08-314892	11/1996	Japan				

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							Yes	No
	AAAAA	2001-068994	03/2001	Japan				
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